

FIG. 1A

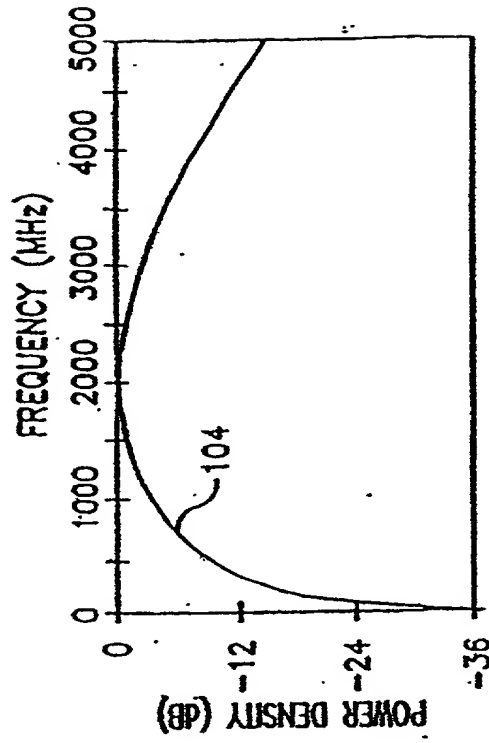


FIG. 1B

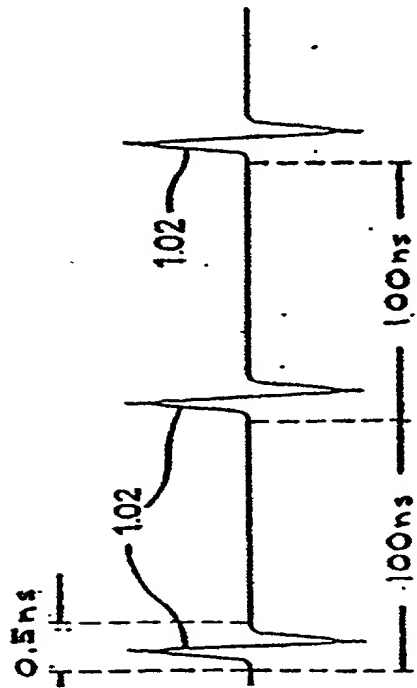


FIG.2A

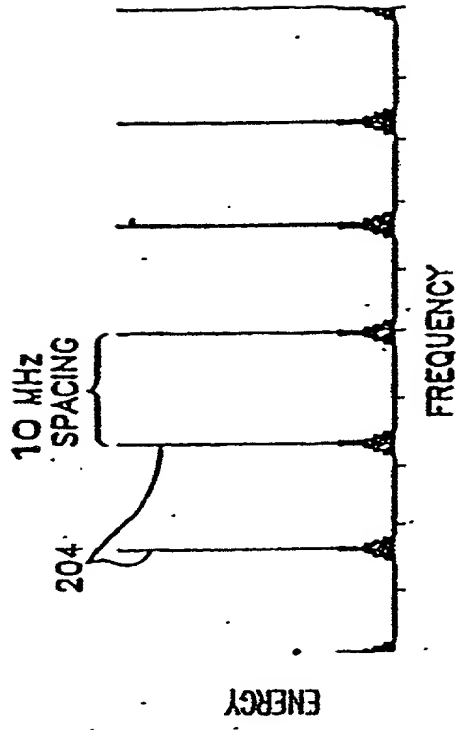


FIG.2B

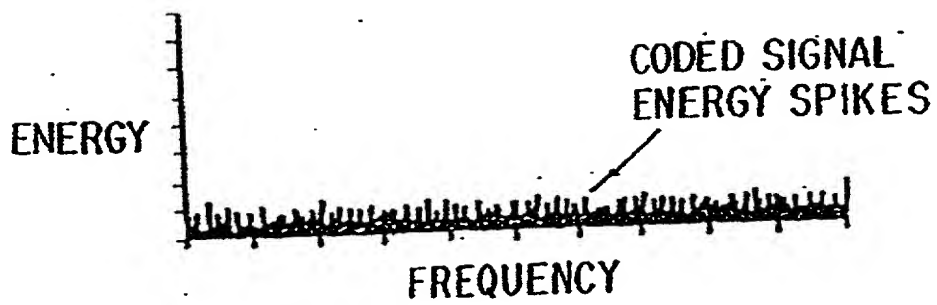


FIG. 3

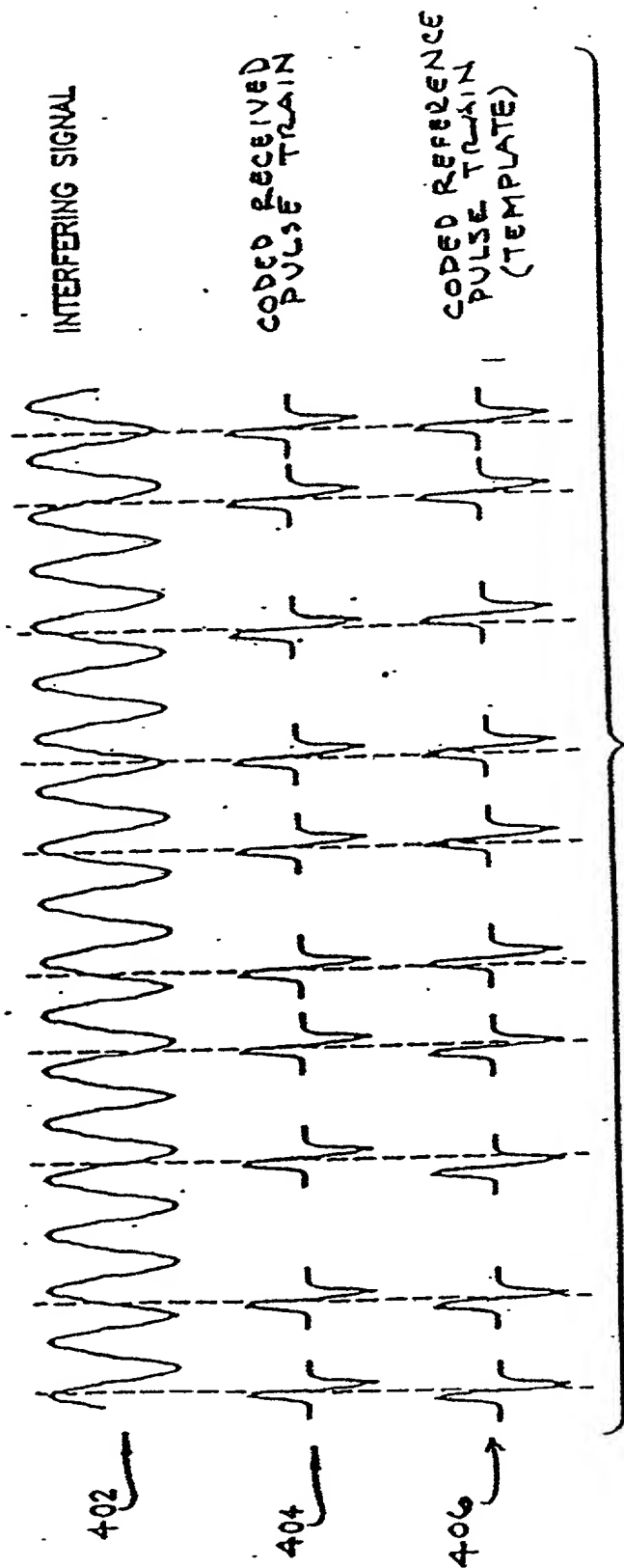


FIG. 4

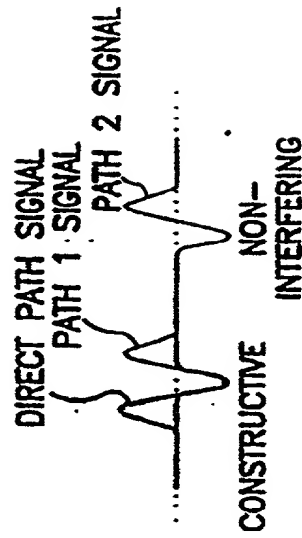
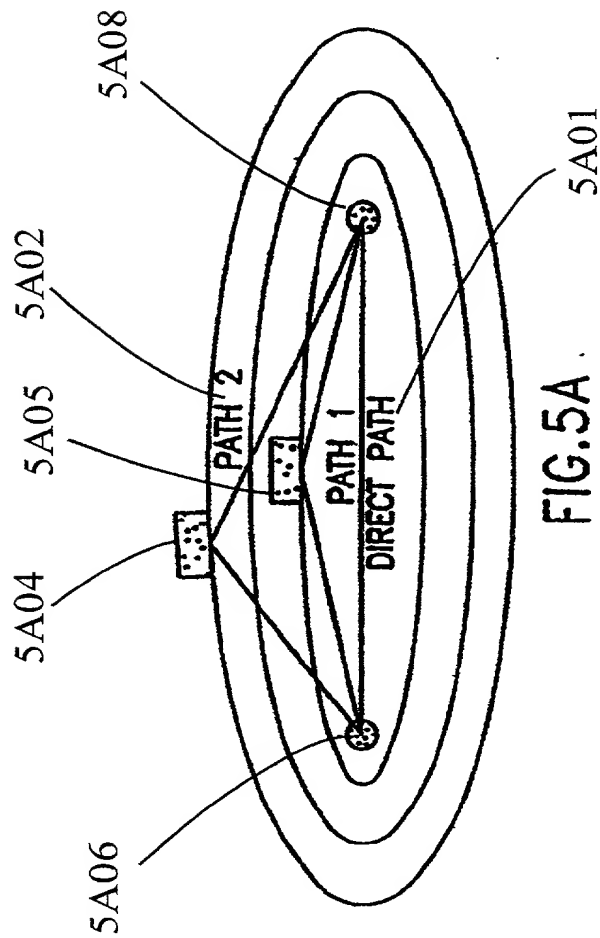
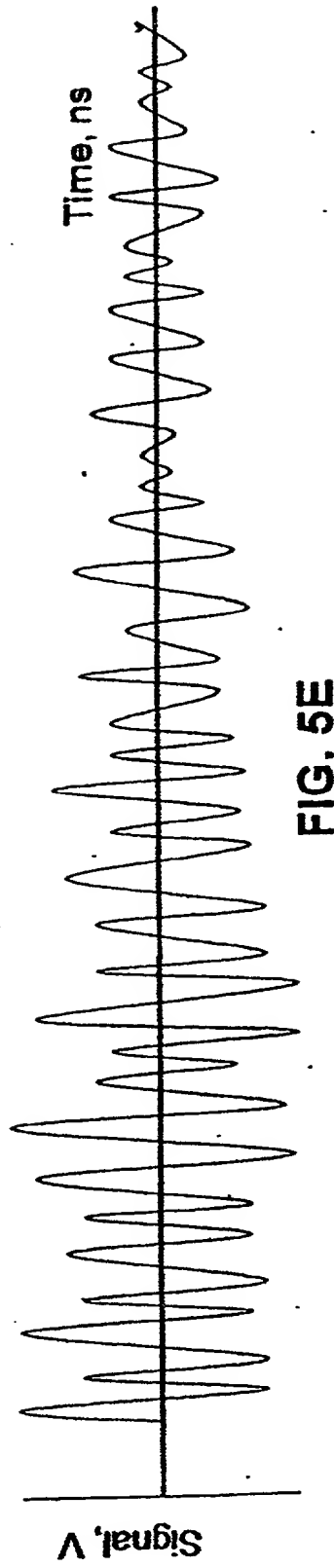
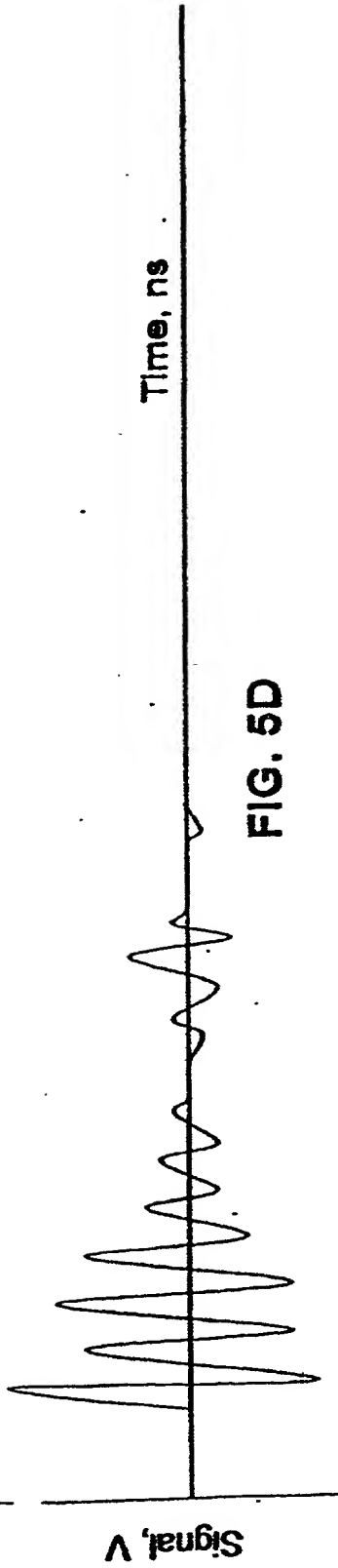
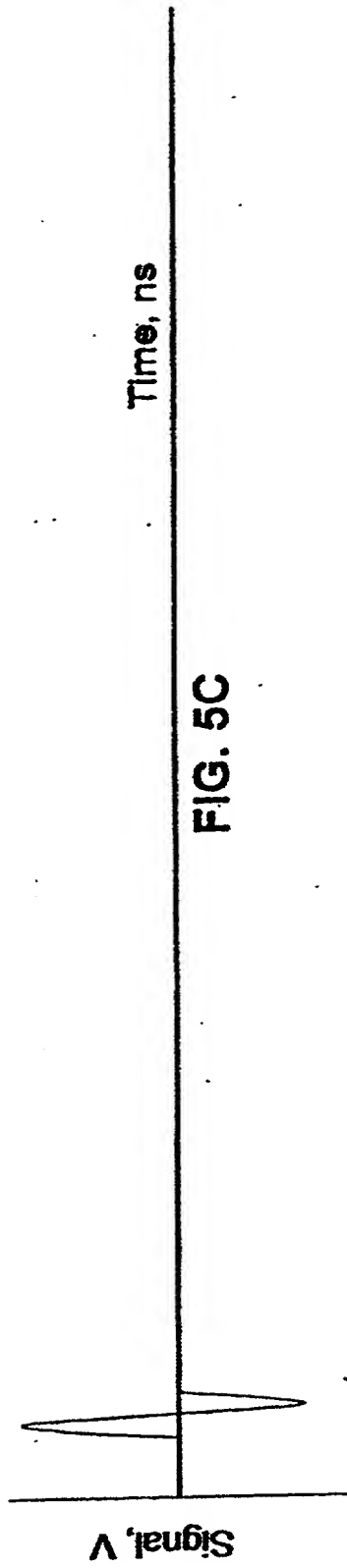


FIG. 5B



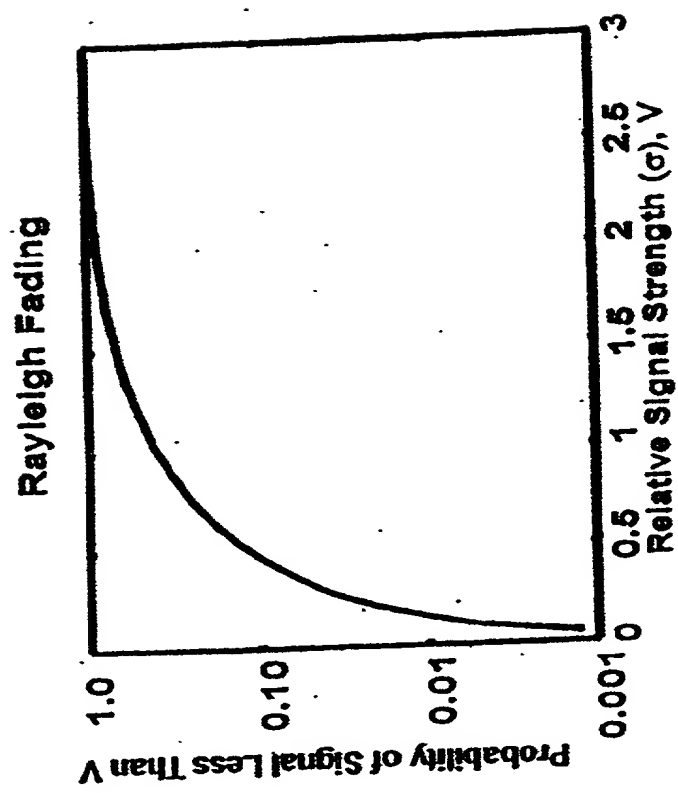


Fig. 5F

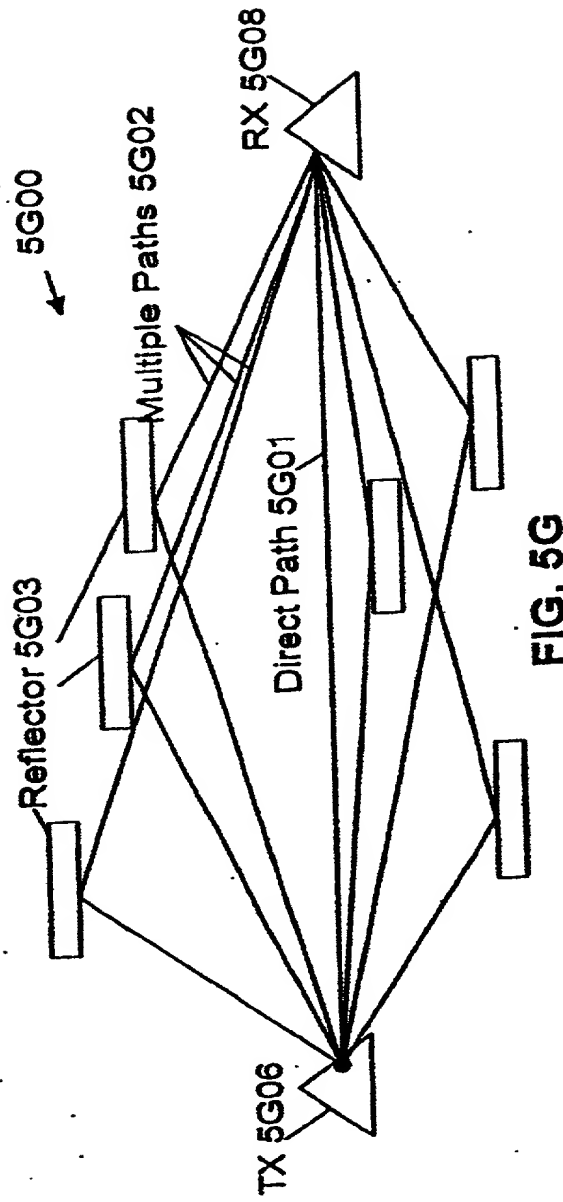


FIG. 5G

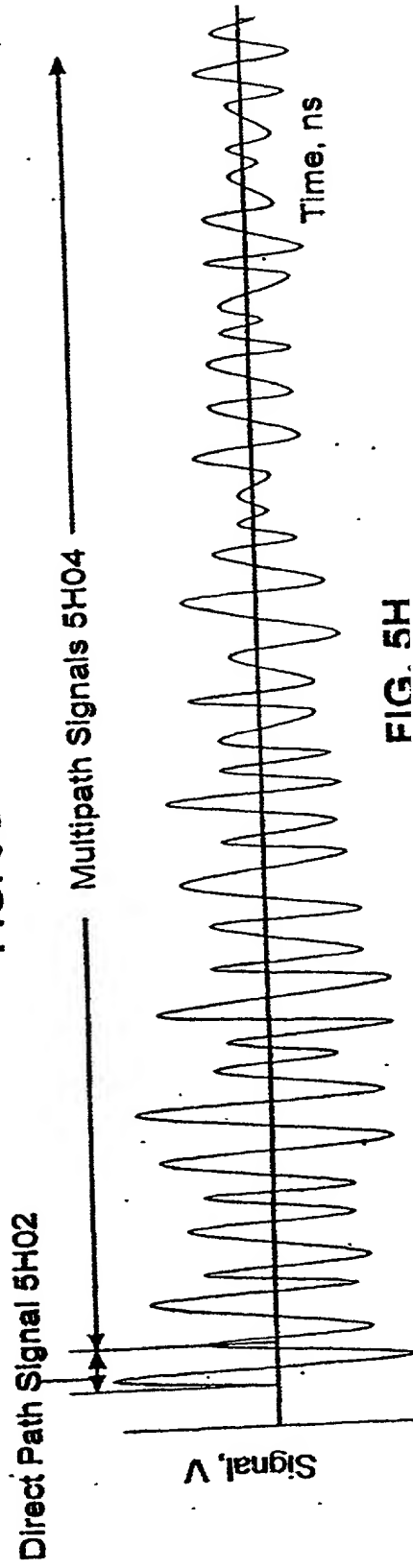


FIG. 5H

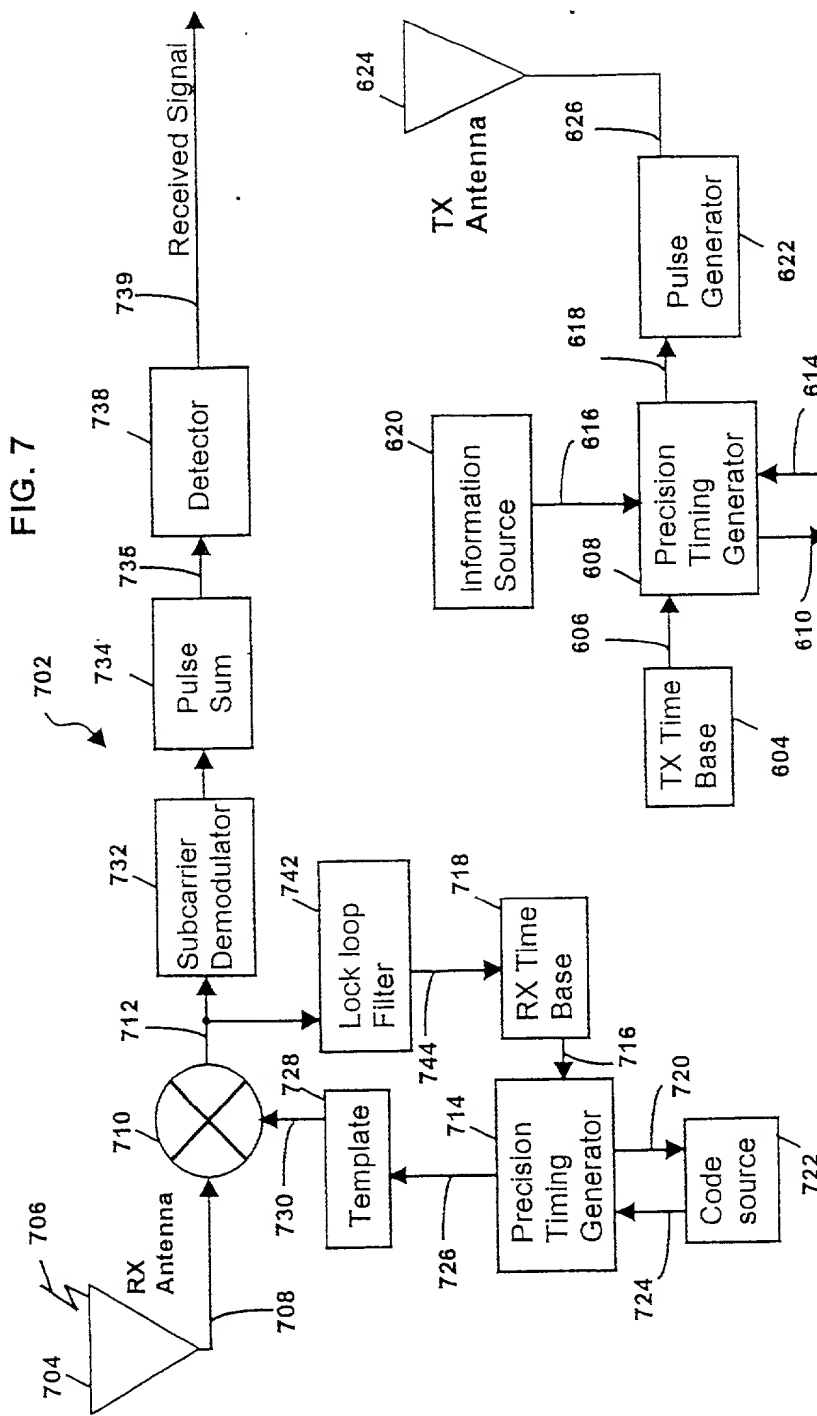


FIG. 7

FIG. 6

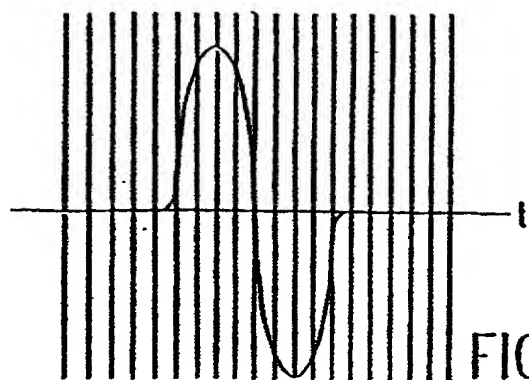


FIG. 8A

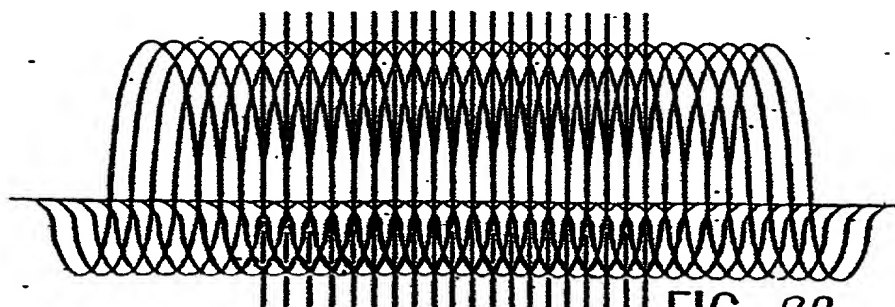
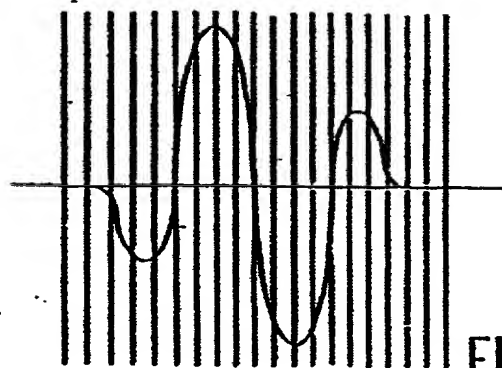


FIG. 8B



CORRESPONDING
TO EACH
 Δt

FIG. 8C

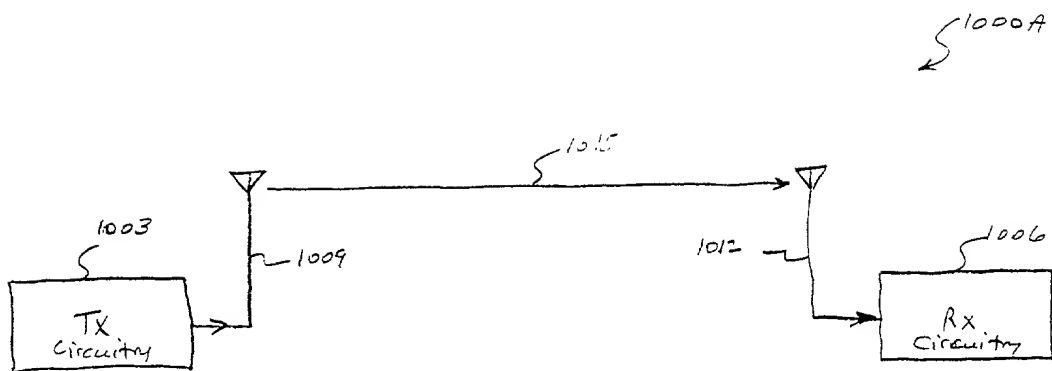


FIG. 9

FIG. 10A

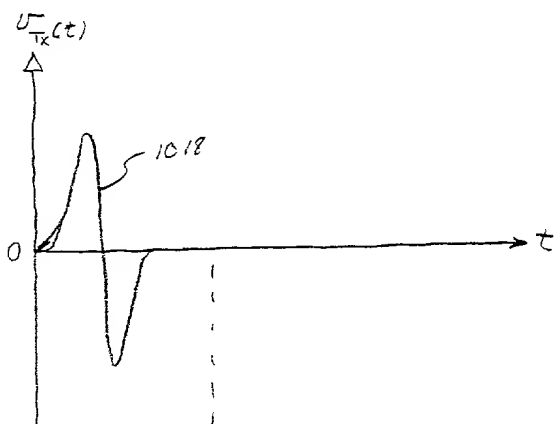
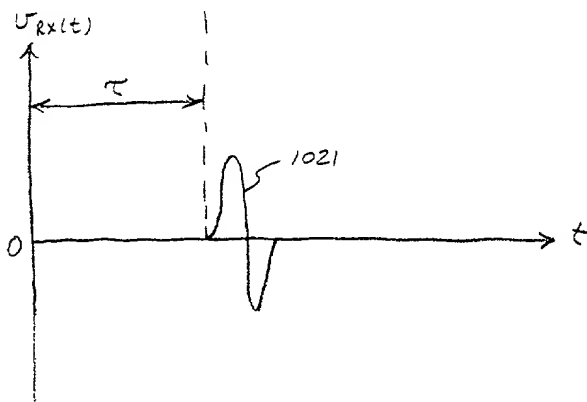


FIG. 10B



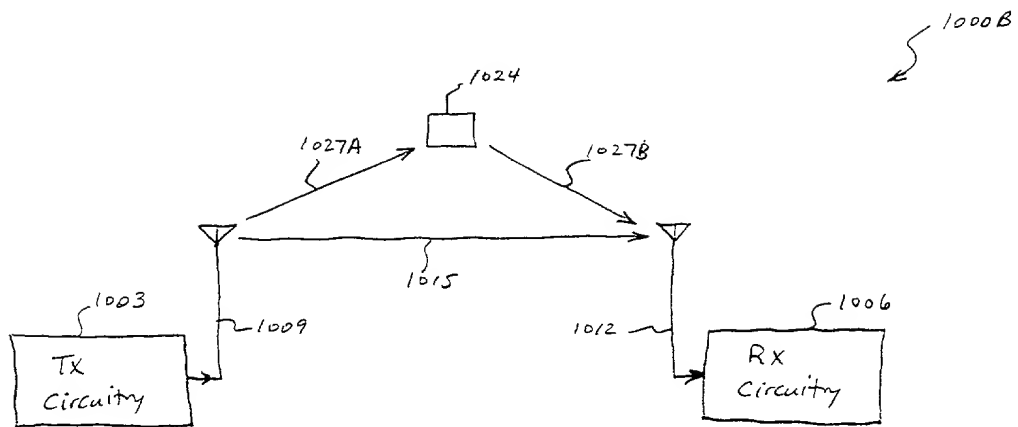
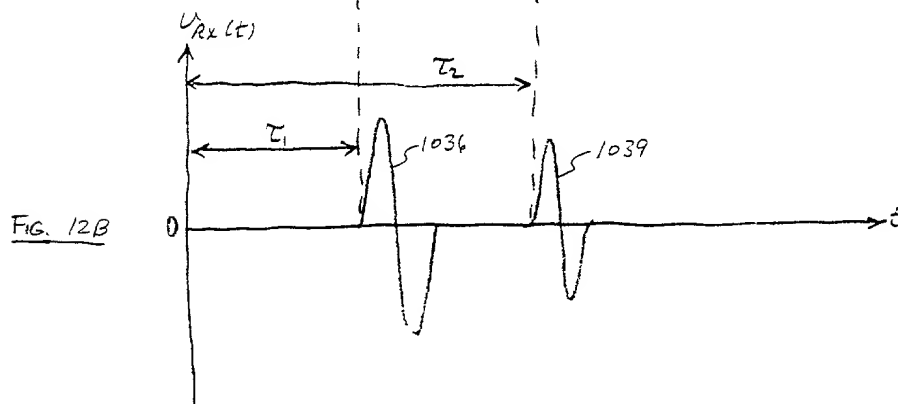
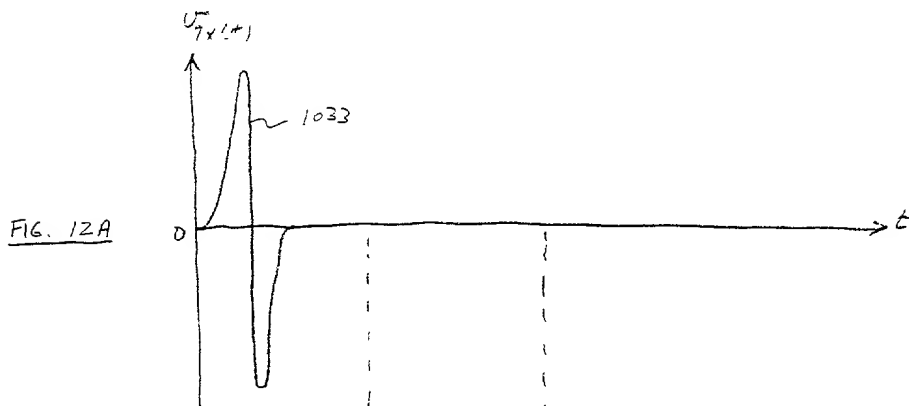


FIG. 11



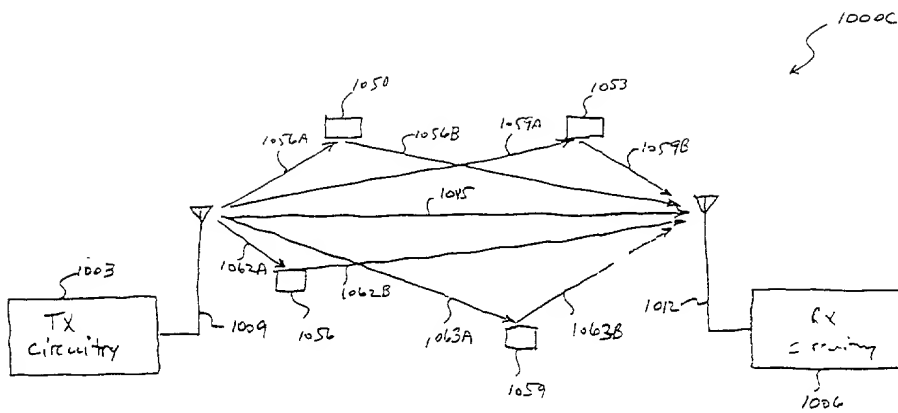


FIG. 13

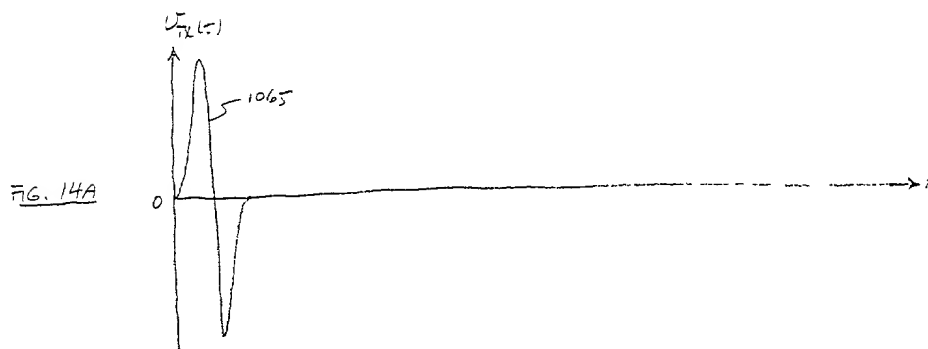


FIG. 14A

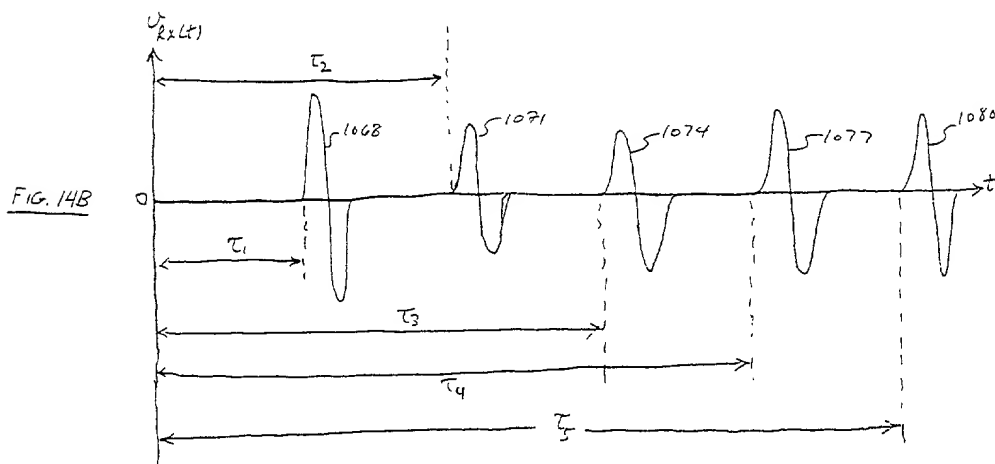
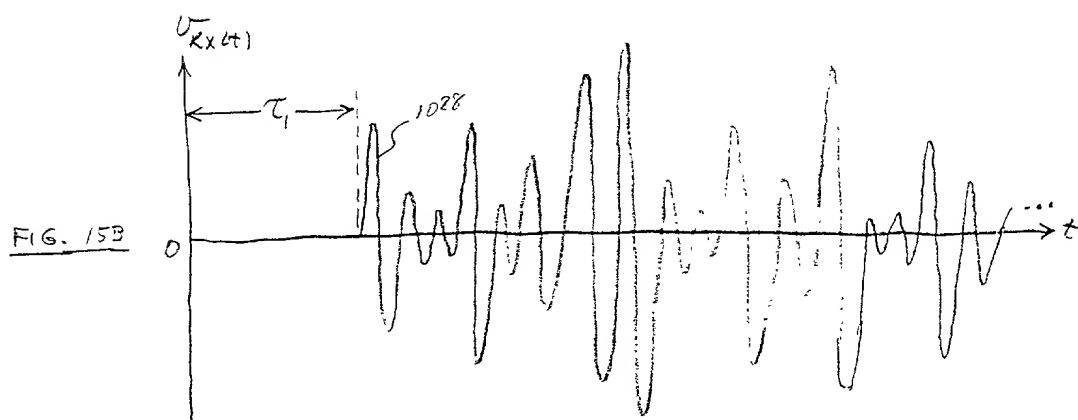
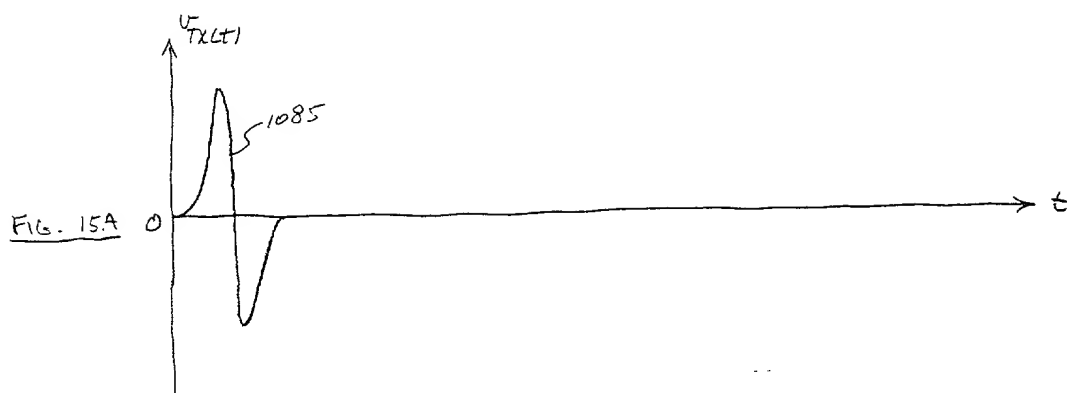


FIG. 14B



1095A

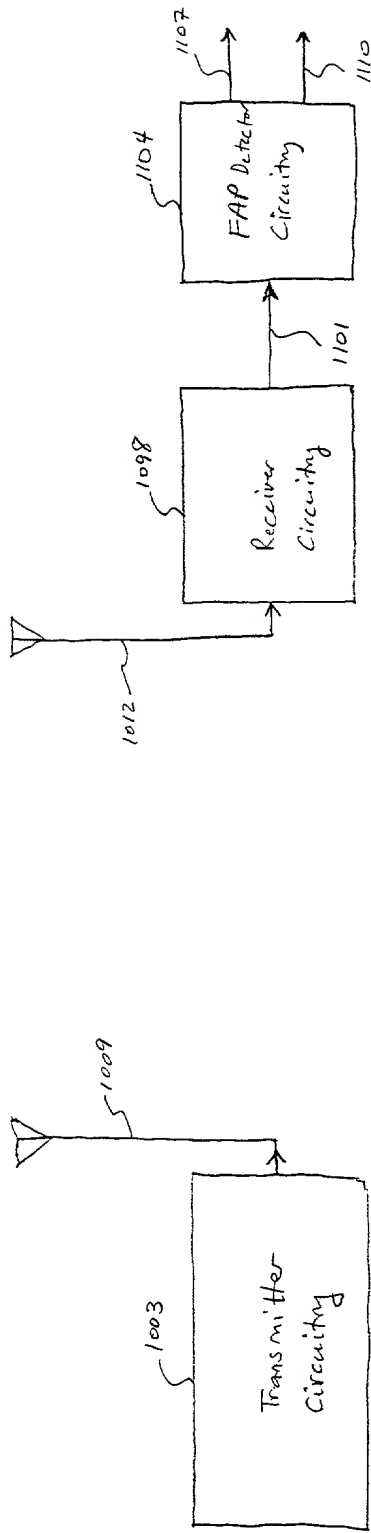


FIG. 16

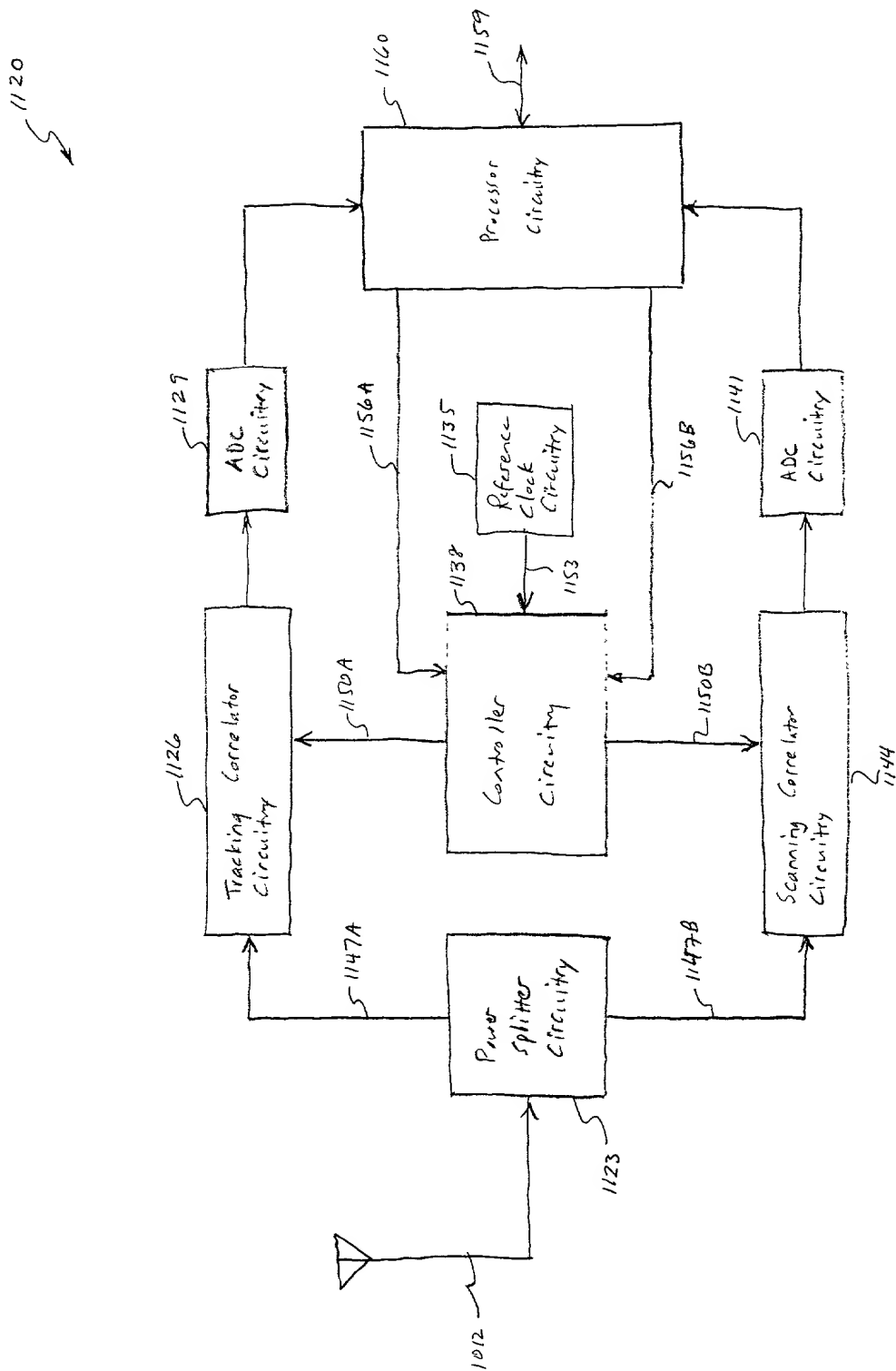


FIG. 17

FIG. 18 is a block diagram of a receiver circuit 1095B.

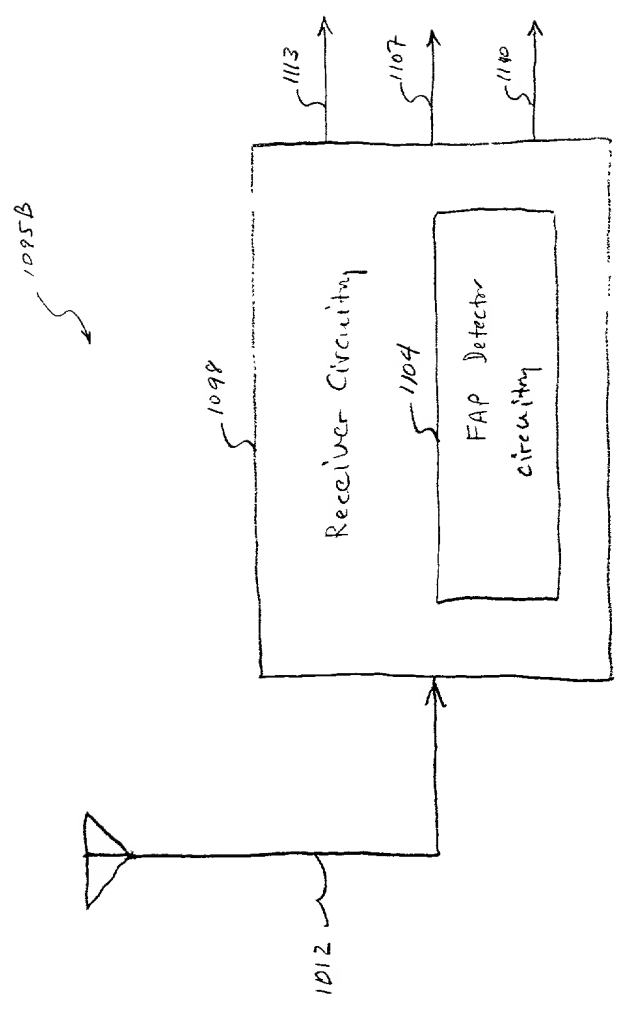


FIG. 18

1095C

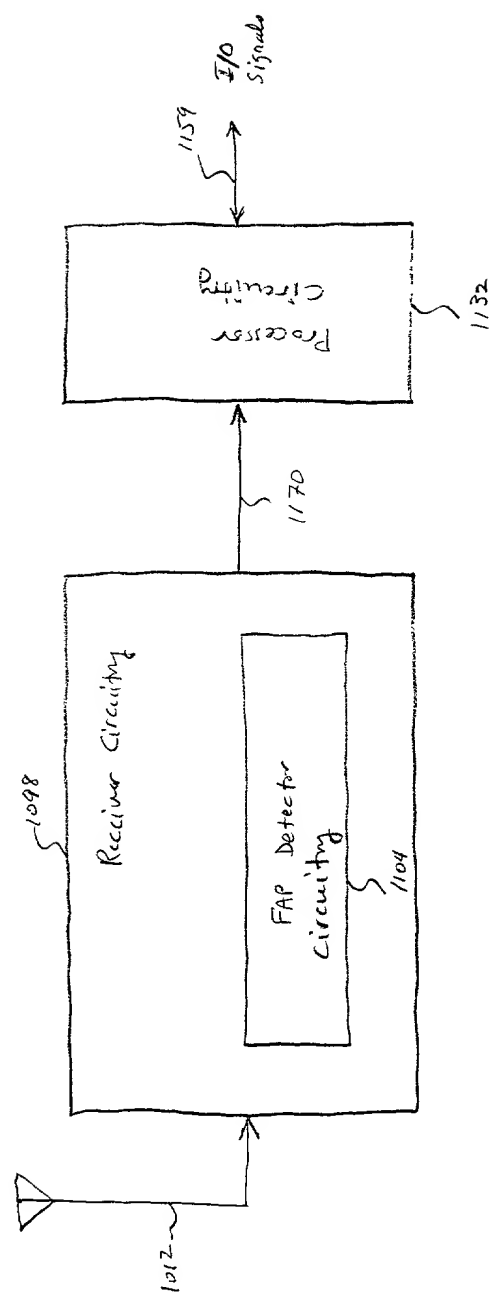


FIG. 19

10950

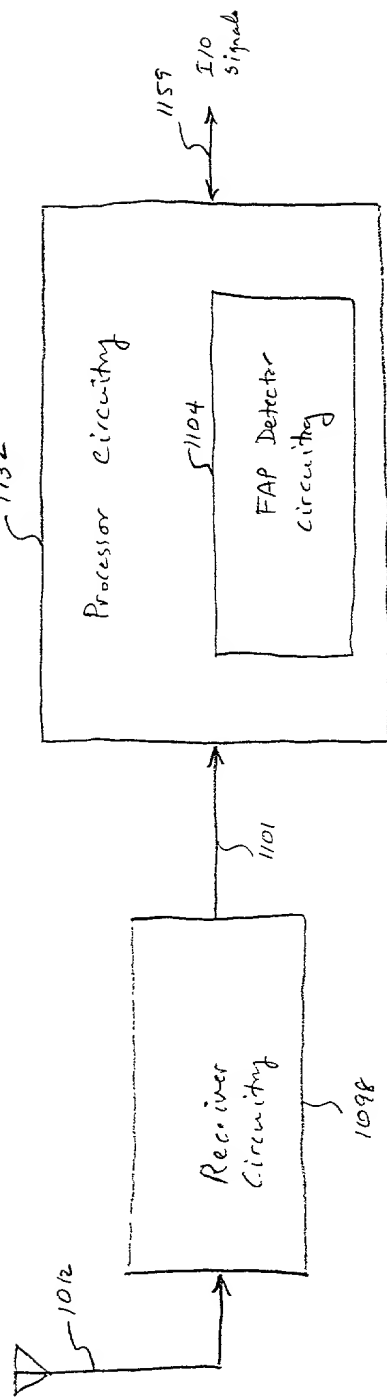


FIG. 20

1095E

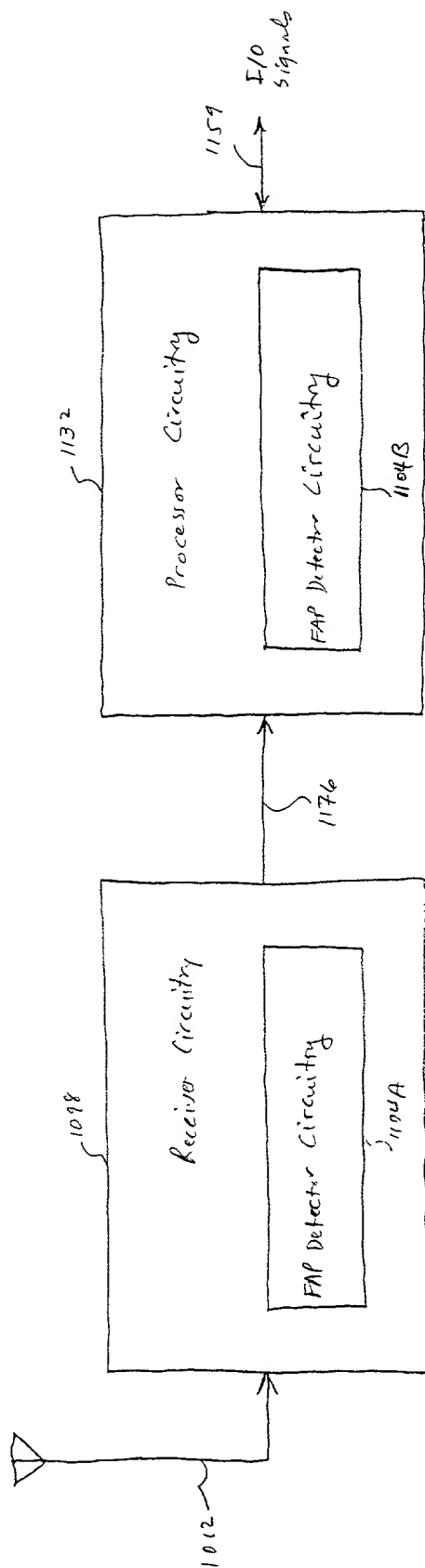


FIG. 21

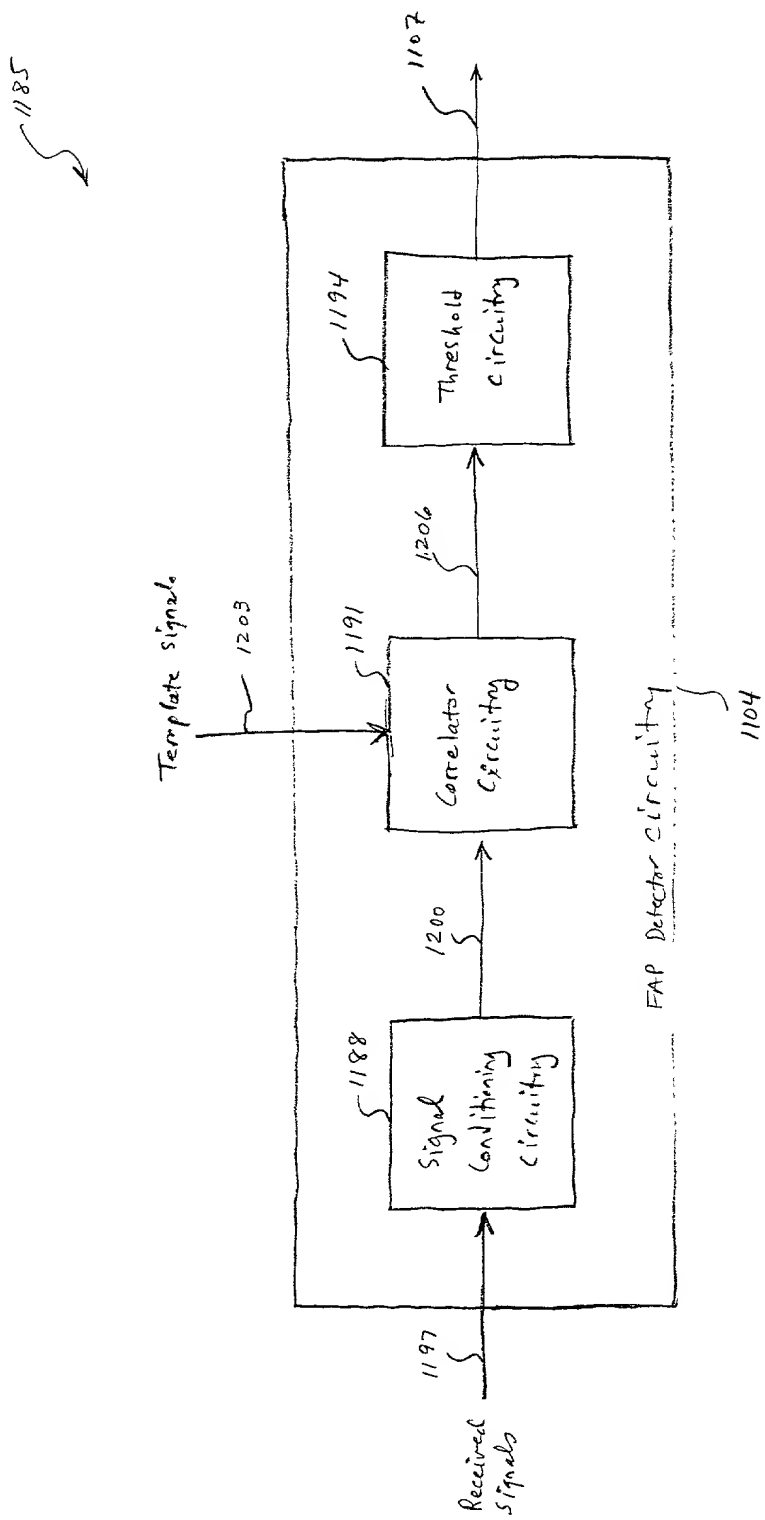


FIG. 22

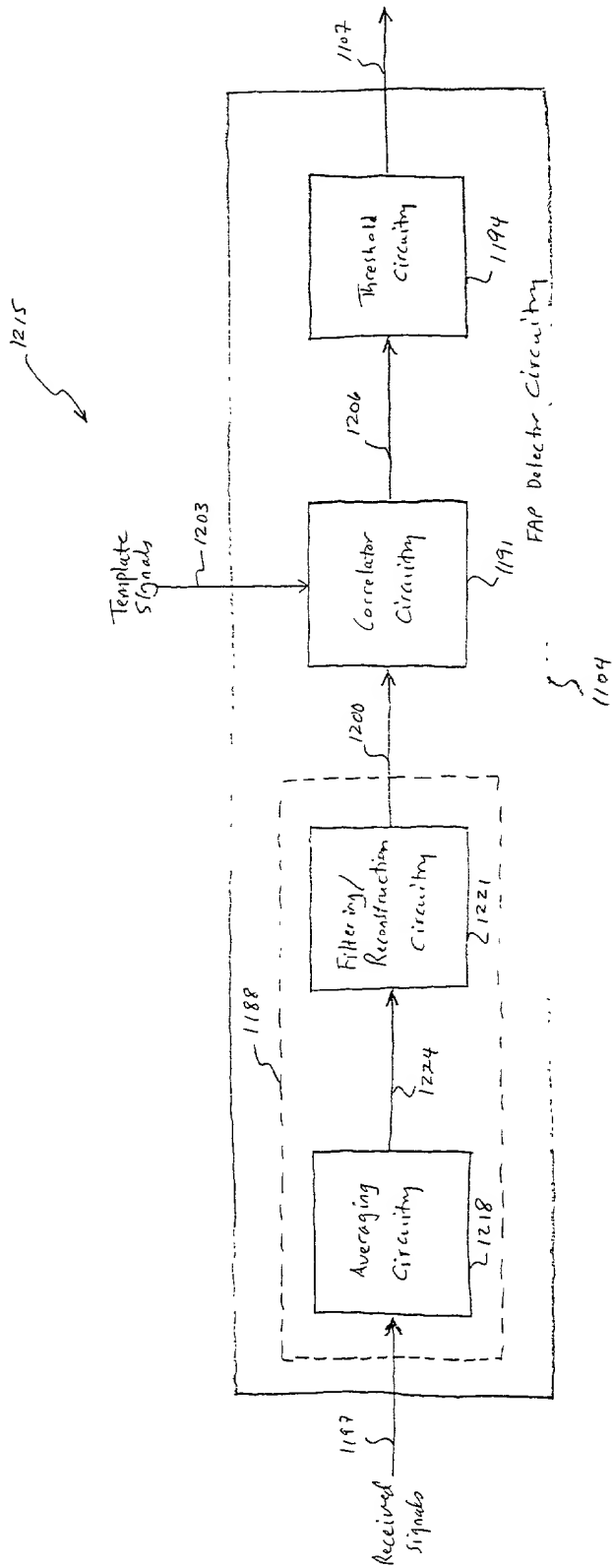


FIG. 23

1235

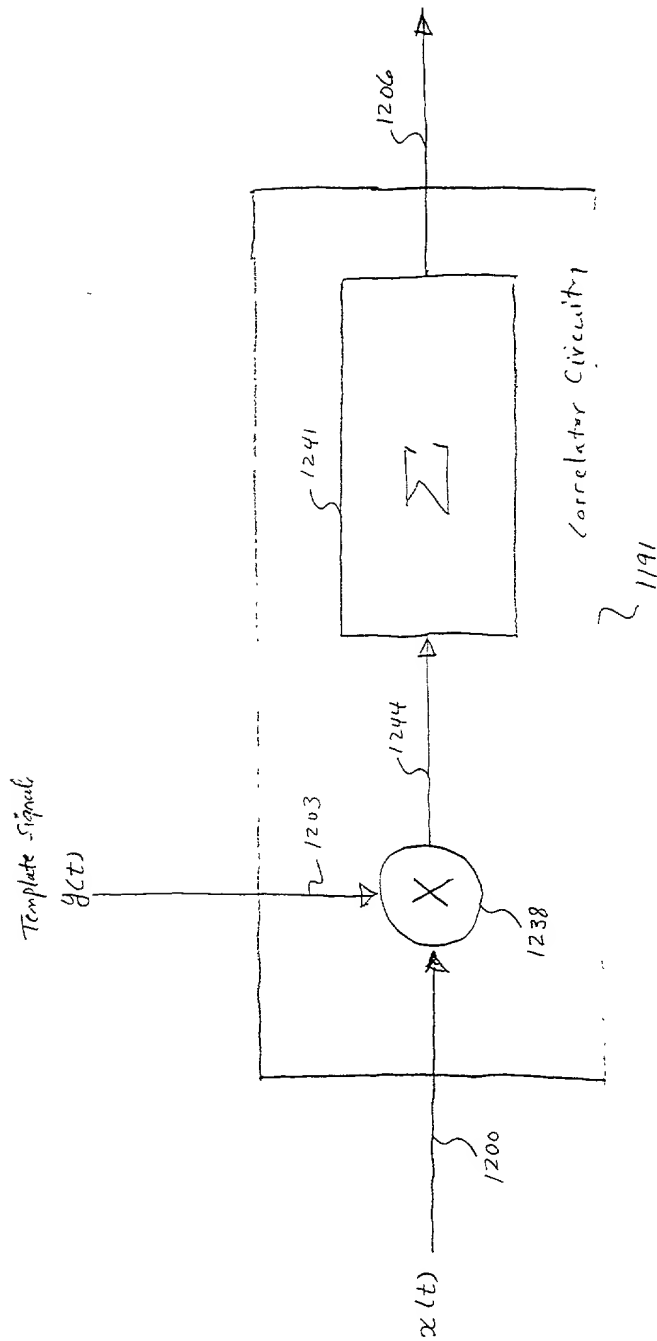


FIG. 24

1250

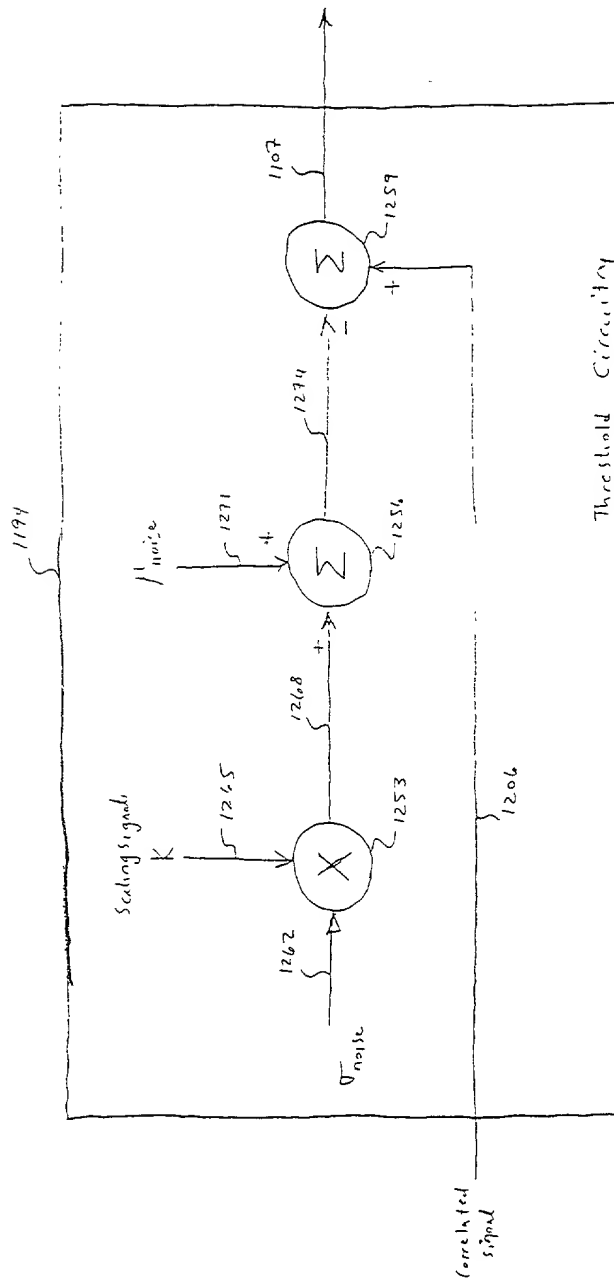


FIG. 25

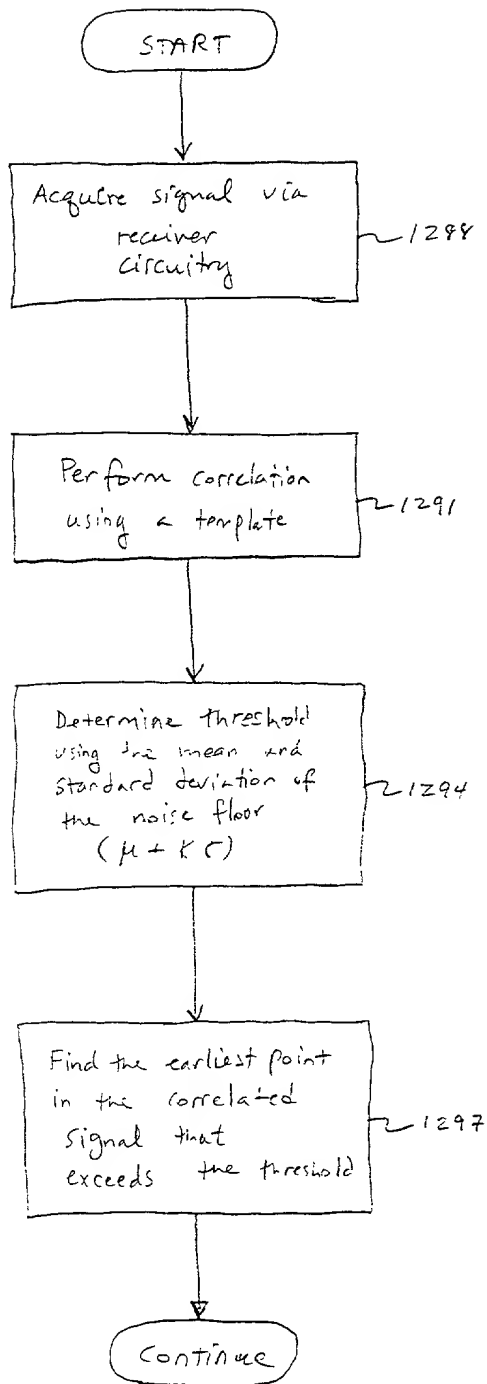


FIG. 26

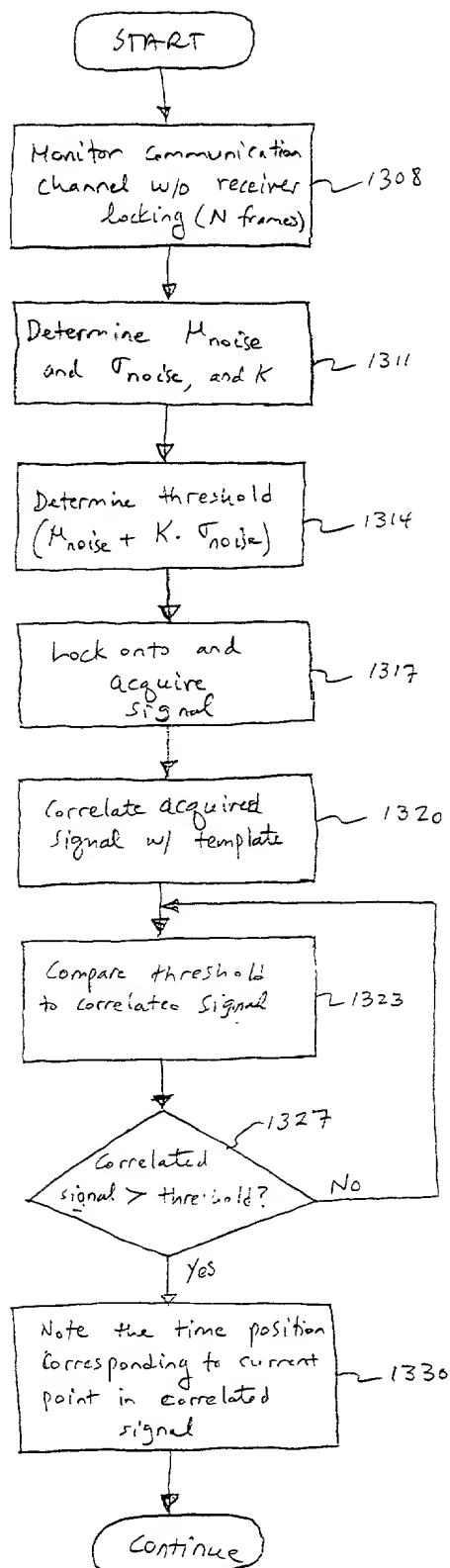


FIG. 27

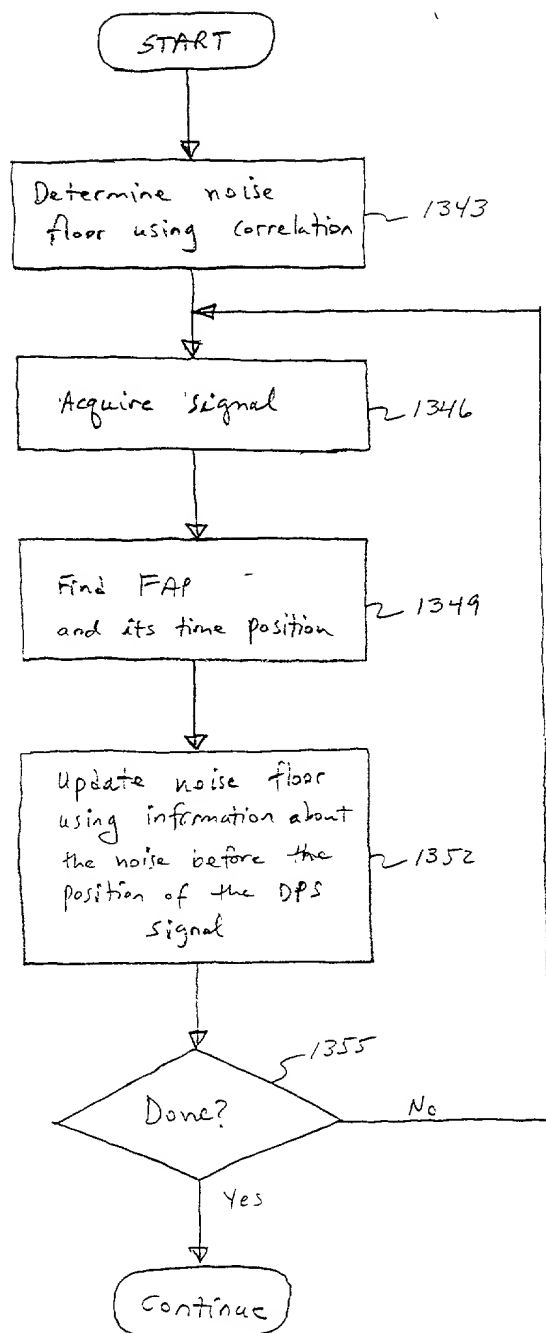


FIG. 28

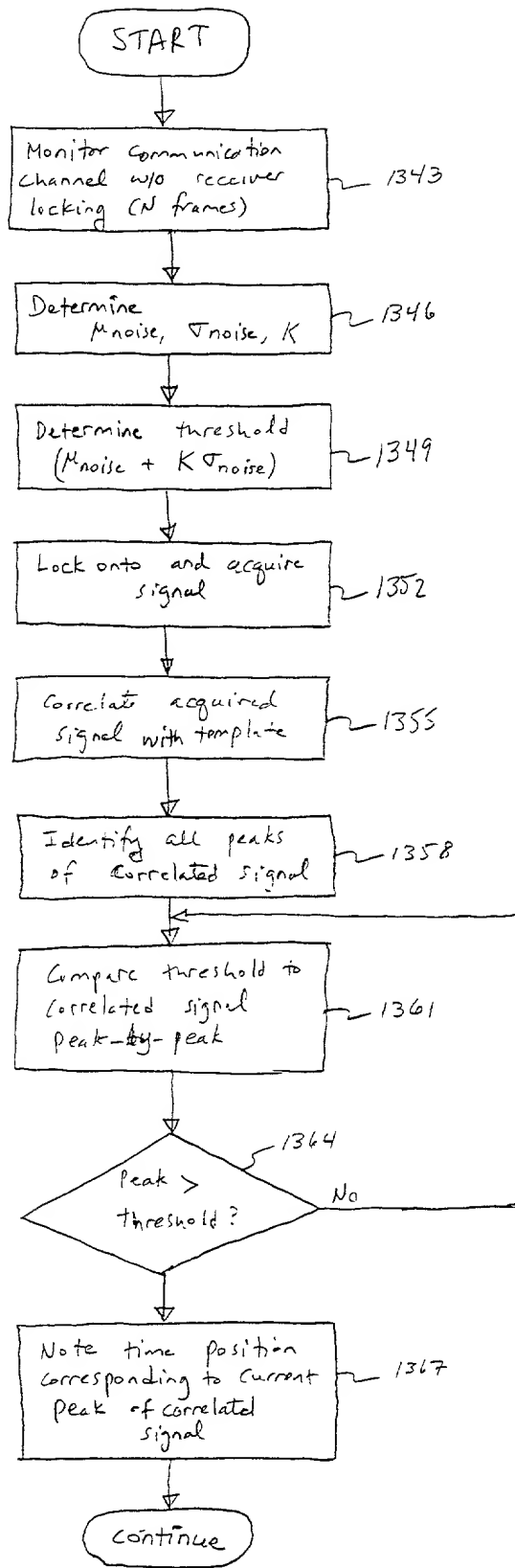


FIG. 29

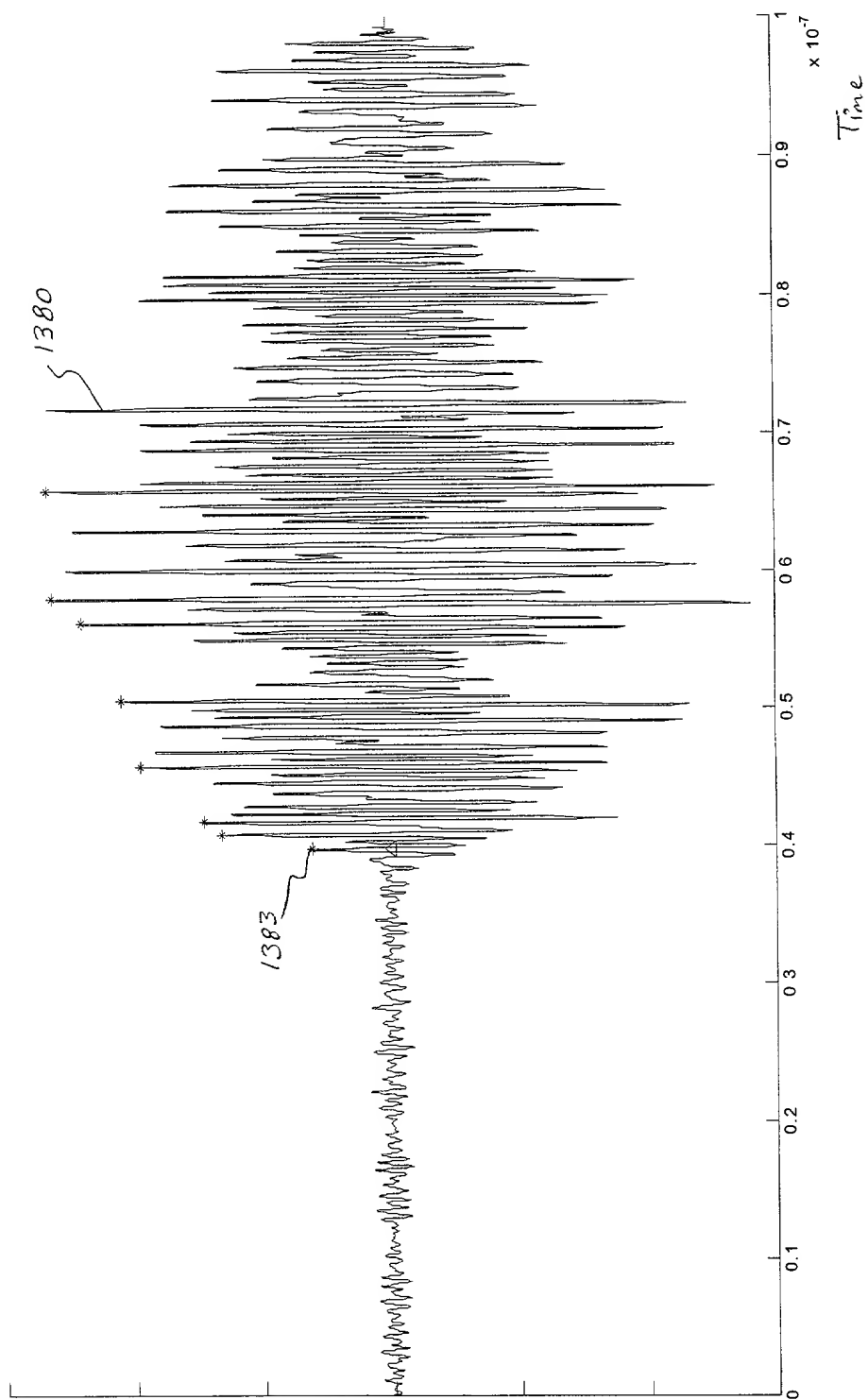


FIG. 3D

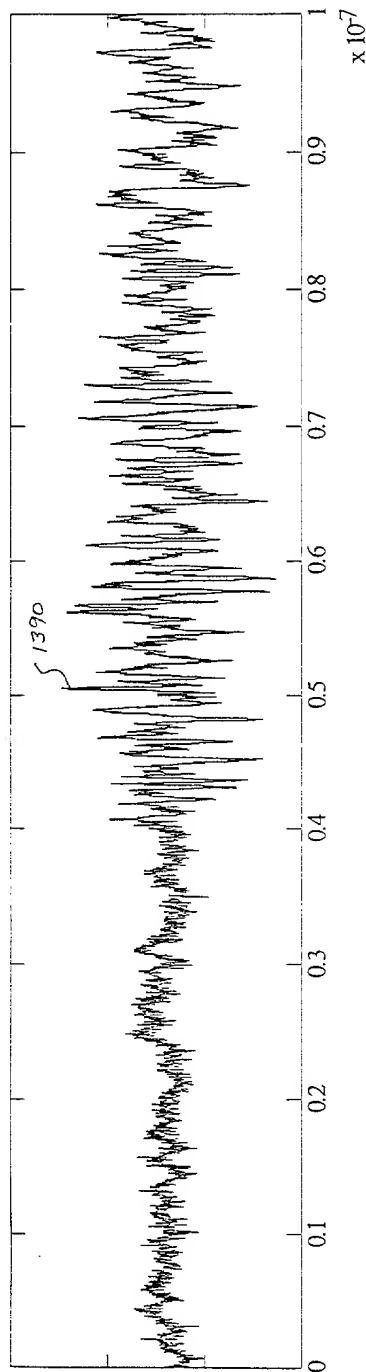


FIG. 31 A

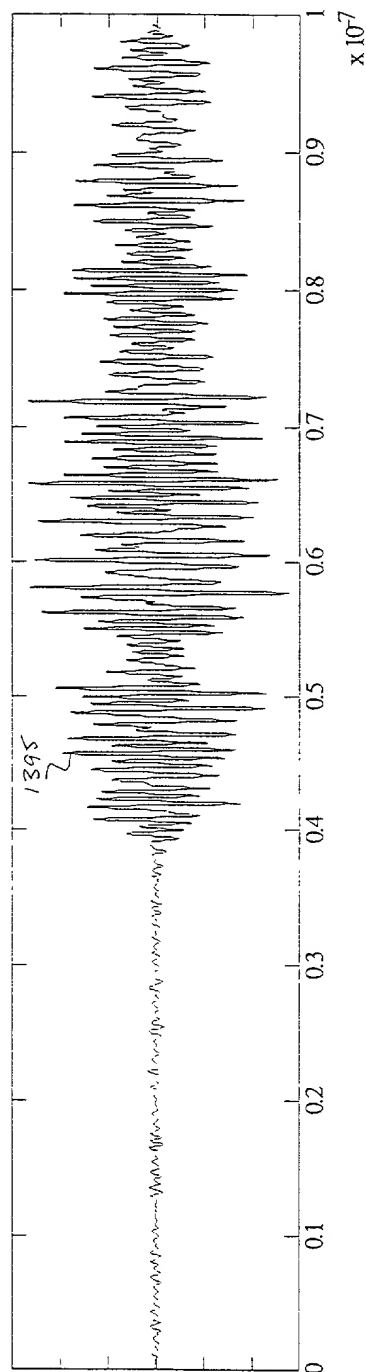


FIG. 31 B

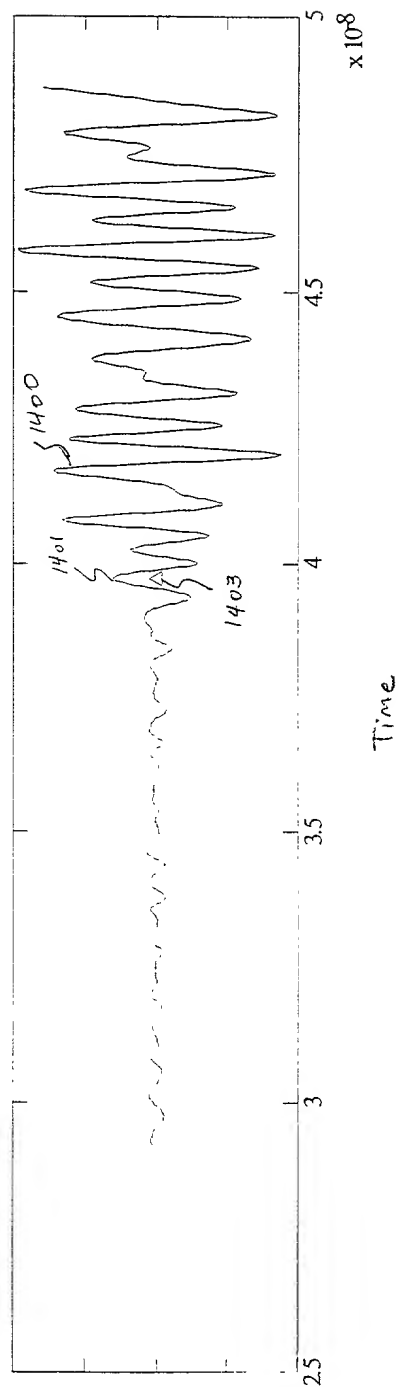


FIG. 31 C

1410

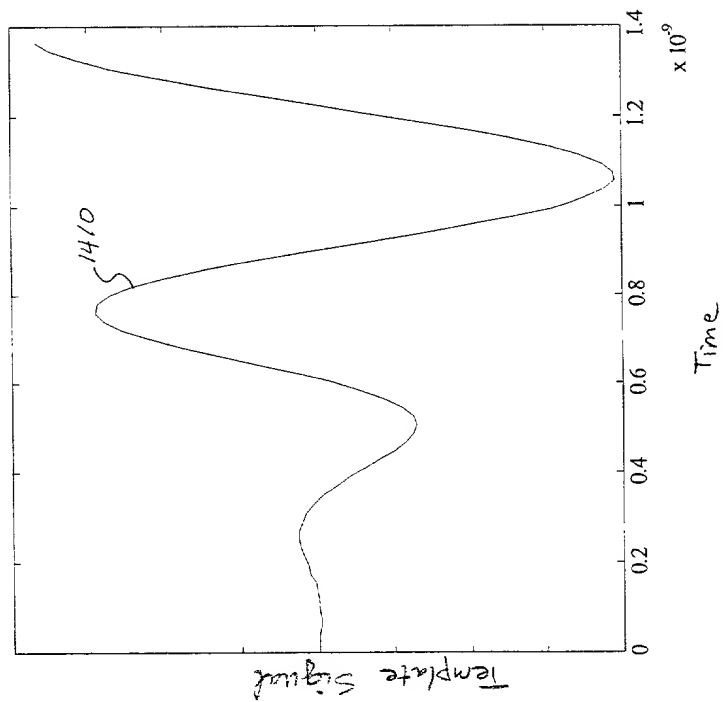


FIG. 32

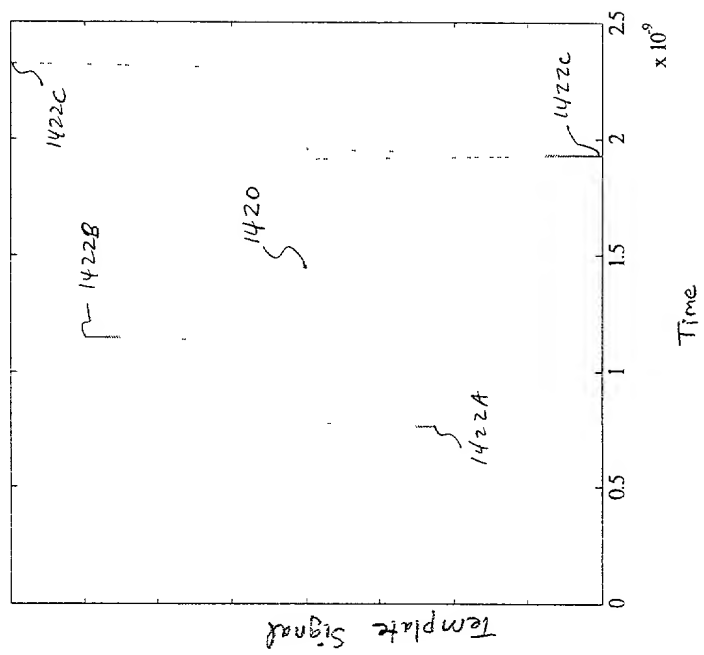


FIG. 33

1430

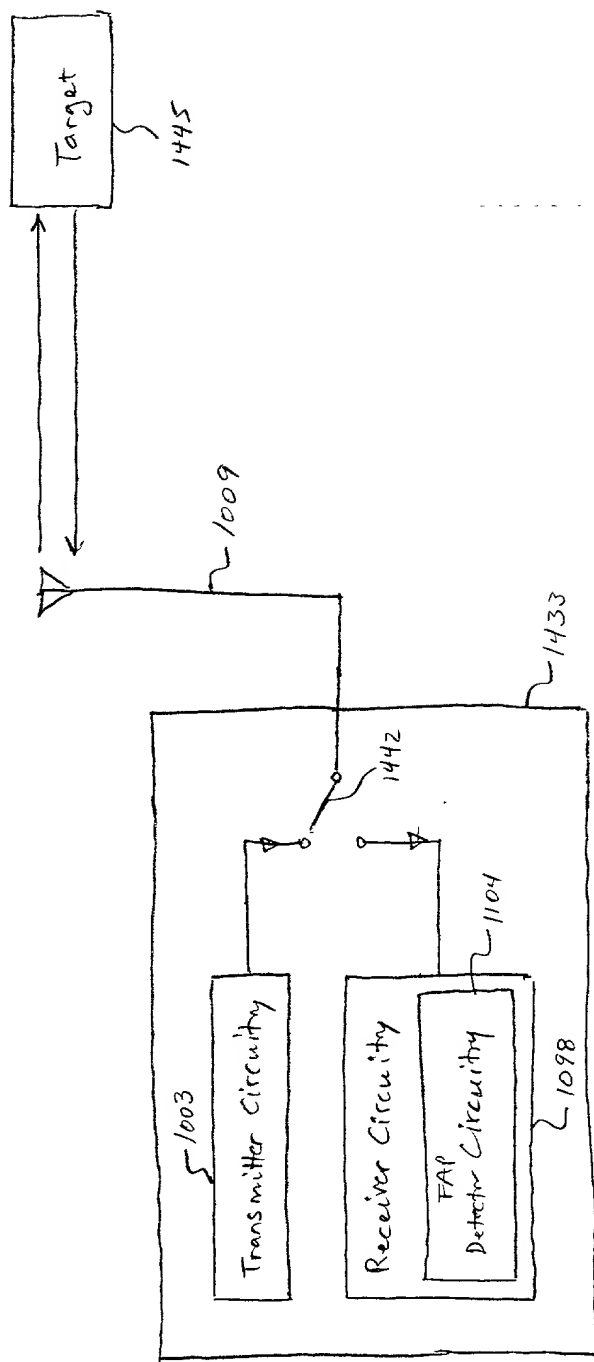


FIG. 34

1450

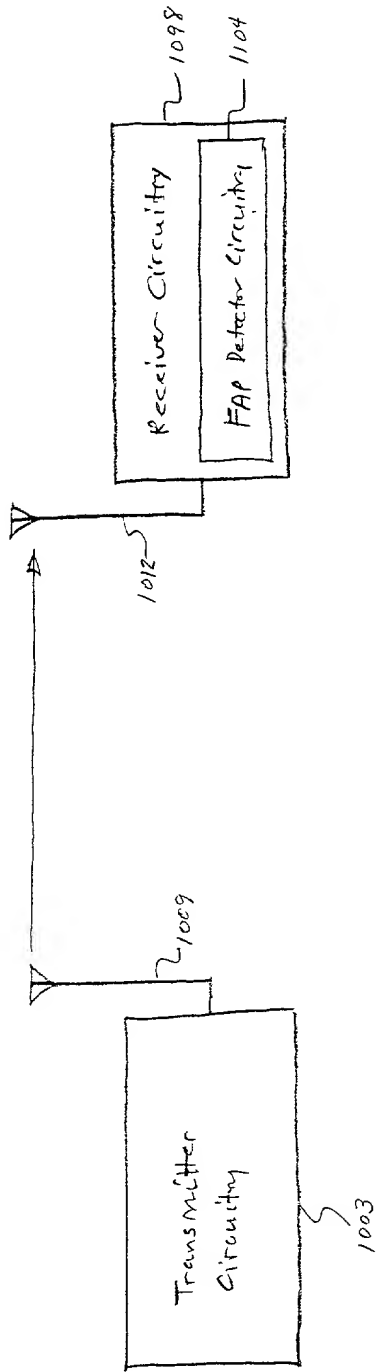


FIG. 35

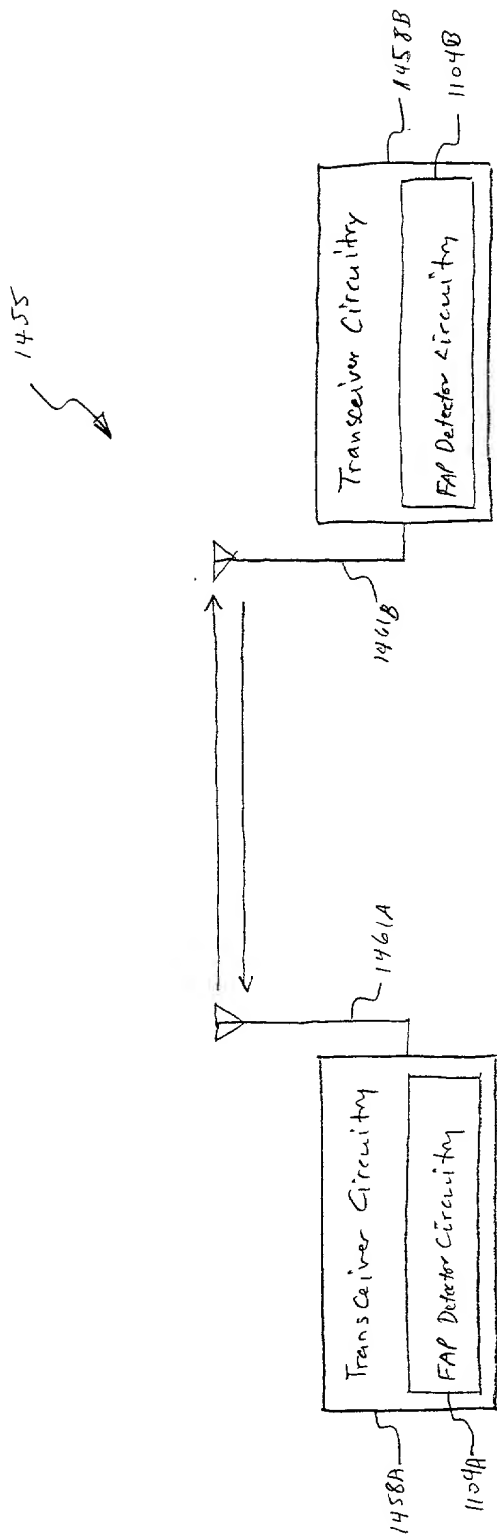


FIG. 36